

Coaxlink Quad G3

PCIe 3.0 four-connection CoaXPress frame grabber (fan-cooled heatsink)



At a Glance

- Four CoaXPress CXP-6 connections: 2,500 MB/s camera bandwidth
- PCIe 3.0 (Gen 3) x4 bus: 3,300 MB/s bus bandwidth
- Feature-rich set of 20 digital I/O lines
- Fan-cooled heatsink
- Extensive camera control functions
- Memento Event Logging Tool

Benefits

PCIe 3.0 (Gen 3) x4 bus

• 3,300 MB/s sustained bus bandwidth

Cooling method

- Fan-cooled heatsink
- Also available with a passive (fanless) heatsink.

Acquire images from the fastest and highest resolution cameras

- Highest data acquisition rate in the industry
- 25 Gbit/s (2,500 MB/s) bandwidth from camera to host PC memory

Power over CoaXPress

- Power over CoaXPress : Feed your camera up to 17 W per channel under 24 VDC with automatic device detection, measurement and overload protection.
- Total and per-channel voltage and current measurement is possible, allowing validation and performance deviation monitoring.

Long cable support

- 40 meters at CXP-6 speed (6.25 Gbps)
- 100 meters at CXP-3 speed (3 Gbps)

Use standard coaxial cables

- A single inexpensive cable for data transfer, camera control, trigger and power supply
- Top reliability and flexibility, performs in the harshest environments

Robust connectors for reliable connections

• Coaxlink CXP-6 uses DIN 1.0/2.3 connectors with push/pull latching system

Connect up to 4 cameras to a single Coaxlink card

Memento Event Logging Tool

- Memento is an advanced development and debugging tool available for Coaxlink and Grablink cards.
- Memento records an accurate log of all the events related to the camera, the frame grabber and its driver as well as the application.
- It provides the developer with a precise timeline of time-stamped events, along with context information and logic analyzer view.
- It provides valuable assistance during application development and debugging, as well as during machine operation.

Direct GPU transfer

- Sample programs for AMD DirectGMA and NVIDIA (CUDA) available.
- Direct GPU transfer eliminates unnecessary system memory copies, lowers CPU overhead, and reduces latency, resulting in significant performance improvements in data transfer times for applications.
- Direct capture of image data to GPU memory is available using AMD's DirectGMA. Compatible with AMD FirePro W5x00 and above and all AMD FirePro S series products.

General purpose I/O lines

- Compatible with a wide range of sensors and motion encoders.
- High-speed differential inputs: Quadrature motion encoder support up to 5 MHz.
- Isolated current-sense inputs: 5V, 12V, 24V signaling voltages accepted, up to 50 kHz, individual galvanic isolation up to 250VDC and 170VAC RMS.
- Isolated contact outputs.
- High-speed 5V-compliant TTL inputs/ LVTTL outputs.

High-performance DMA (Direct Memory Access)

- Direct transfer into user-allocated memory and hardware boards that expose PCI addresses
- Hardware scatter-gather support
- 64-bit addressing capability

Area-scan triggering capabilities

- A trigger is used to start the acquisition when the part is in position. Hardware triggers come from the Coaxlink's I/O lines. Software triggers come from the application.
- An optional trigger delay is available to postpone the acquisition for a programmable time.
- A trigger decimation function allows to skip some of the triggers.
- Camera exposure control allows the application to control the exposure time of the camera.
- When the acquisition starts, at the appropriate timing, the Coaxlink board generates a signal to control an illumination device connected to one of its output lines.

Line-scan triggering capabilities 1/2

Coaxlink supports continuous web scanning (to inspect infinite, continuously moving surfaces without losing a single line) and discrete object scanning (to acquire the image of objects moving in front of the camera).

- A trigger is used to start the acquisition when the part is in position. Hardware triggers come from the board's I/O lines. Software triggers come from the application.
- After it is started, the acquisition either:
 - [–] Continues indefinitely (for web inspection applications)
 - ⁻ Continues for a programmable number of lines (to acquire the image of objects of a known length)
 - ⁻ Continues until an end trigger is received (to acquire the image of objects of a variable length)
- An optional trigger delay is available to postpone the beginning of the acquisition for a programmable number of lines.

Line-scan triggering capabilities 2/2

- The Coaxlink frame grabber controls the camera scanning rate based on the signals received from a motion encoder. When the parts move faster, the acquisition line rate of the camera increases. When the parts move slower, the acquisition line rate of the camera decreases.
- The Coaxlink boards interpret A/B signals from quadrature motion encoders to know in which direction (forward or backward) the part is moving.
- Optionally, the Coaxlink board can be instructed to acquire lines only when the object is moving forward or only when the object is moving backward.
- A feature called Backward Motion Cancellation stops the acquisition when a backward motion is detected. The line acquisitior automatically resumes when the motion is again in the forward direction, at the exact place where the acquisition was interrupted.
- A Rate Converter allows the camera to acquire lines at any programmable resolution lower or higher than the resolution of the motion encoder. This gives the designer incredible freedom and flexibility during the development of the application.
- A Rate Divider allows the camera to acquire lines at a resolution lower than the resolution of the motion encoder. It divides the frequency of the incoming encoder signal by a programmable integer.

Flexible line-scan camera operation with the rate converter

- The rate converter is a smart, programmable frequency multiplier/divider.
- Used with motion encoders and line-scan cameras, it allows the user to choose the aspect ratio of the pixels in the image.
- It provides a way to calibrate the acquisition chain to easily reach square (1:1 aspect ratio) pixels.

C2C-Link camera synchronization

Allows to accurately synchronize multiple area-scan or line-scan cameras connected

- to the same card
- to different cards in the same PC
- to different cards in different PCs

Compatible with eGrabber

- eGrabber Studio: eGrabber's new interactive evaluation and demonstration application
- GenICam Browser: An application giving access to the GenICam features exposed by the GenTL Producer(s)
- GenTL Console: A command-line tool giving access to the functions and commands exposed by the Euresys GenTL Producer

Compliant with GenICam

Including support for

- GenApi
- The Standard Feature Naming Convention (SFNC)
- GenTL

Windows, Linux and macOS drivers available

• Including support for Intel 64-bit platforms as well as ARM 64-bit platforms

Applications

Machine Vision for the Electronic Manufacturing Industry

- High speed image acquisition for AOI, 3D SPI, 3D lead/ball inspection machines.
- Very high resolution line-scan image acquisition for Flat Panel Display inspection and solar cell inspection

Machine Vision for the General Manufacturing Industries

- High frame rate image acquisition for inspection machines
- Line-scan image acquisition for surface inspection machines
- Line-scan image acquisition for textile inspection
- Image acquisition for robots

Machine Vision for the Printing Industry

• High speed line-scan image acquisition for printing inspection machines

Video Acquisition and Recording

• High-frame-rate video acquisition for motion analysis and recording

Video Monitoring, Surveillance & Security

• Transmission and acquisition of high-definition video over long coaxial cables for traffic surveillance, monitoring and control

Specifications

Mechanical

Format	Standard profile, half length, 4-lane PCI Express card
Cooling method	Air cooling, fan-cooled heatsink
Mounting	For insertion in a standard height, 4-lane or higher, PCI Express card slot
Connectors	• 'A', 'B', 'C', 'D' on bracket:
	 4x DIN 1.0/2.3 female connectors
	 CoaXPress host interface
	 'EXTERNAL I/O' on bracket:
	 26-pin 3-row high-density female sub-D connector
	 I/O lines and power output
	 'INTERNAL I/O 1' and 'INTERNAL I/O 2' on PCB:
	– 2x 26-pin 2-row 0.1" pitch pin header with shrouding
	 I/O lines and power output
	 'AUXILIARY POWER INPUT' on module:
	 – 6-pin PEG power socket
	 12 VDC power input for PoCXP camera(s) and I/O power
	 'C2C-LINK' on module:
	 6-pin 2-row 0.1-in header
	 Card to card link
LED indicators	'A', 'B', 'C', 'D' on bracket:
	 Bi-color red/green LEDs
	 CoaXPress Host connector indicator
	 'FPGA STATUS LAMP' on PCB:
	 Bi-color red/green LED
	 FPGA status indicator
	 'BOARD STATUS LAMP' on PCB:
	 Bi-color red/green LED
	 Board status indicator
Switches	'RECOVERY' on PCB:
	 3-pin 1-row 0.1" header or 2-way DIP switch
	Firmware emergency recovery
Dimensions	PCB L X H: 167.65 mm x 111.15 mm, 6.6 in x 4.38 in
Weight	180 g, 6.35 oz
Host bus	
Standard	PCI Express 3.0

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Link width	• 4 lanes
	1 lane or 2 lanes with reduced performance
Link speed	• 8.0 GT/s (PCIe 3.0)
	• 5.0 GT/s (PCIe 2.0) with reduced performance
Maximum payload size	512 bytes
DMA	32- and 64-bit
Peak delivery bandwidth	3,900 MB/s
Effective (sustained) delivery bandwidth	3,350 MB/s (Host PC motherboard dependent)
Power consumption	Typ. 16.8 W (3.8 W @ +3.3V, 13 W @ +12V), excluding camera and I/O power output
Camera / video inputs	
Interface standard(s)	CoaXPress 1.0, 1.1 and 1.1.1
Connectors	Four DIN1.0/2.3 75 Ohms CXP-6
Status LEDs	One CoaXPress Host connection status LED per connection
Number of cameras	Area-scan cameras:
	 One 1- or 2- or 4-connection camera
	 One 1- or 2- or 4-connection multi-stream camera (up to 4 data streams)
	 One or two 1- or 2-connection cameras
	 One 1- or 2-connection and one or two 1-connection cameras
	 Up to four 1-connection cameras
	 One 4-connection sub-link of an 8-connection camera
	Line-scan cameras:
	 One 1- or 2- or 4-connection camera
	 One or two 1- or 2-connection cameras
	 Up to four 1-connection cameras
Maximum aggregated camera data transfer rate	25 Gbit/s (2,500 MB/s)
Supported CXP down-connection speeds	1.25 GT/s (CXP-1), 2.5 GT/s (CXP-2), 3.125 GT/s (CXP-3), 5 GT/s (CXP-5), and 6.25 GT/s (CXP-6)
Number of CXP data streams (per	• 4 on '1-camera, 4 data-stream' firmware variant
camera)	 1 per camera on other firmware variants
Maximum CXP stream packet size	16,384 bytes
PoCXP (Power over CoaXPress)	PoCXP Safe Power:
	 17 W of 24V DC regulated power per CoaXPress connector
	 PoCXP Device detection and automatic power-on
	 Overload and short-circuit protections
	On-board 12V to 24V DC/DC converter
	 A +12V power source must be connected to the AUXILIARY POWER INPUT connector usin a 6-pin PEG cable
Camera types	Area-scan cameras:
	 Grayscale and color (YCbCr, YUV, RGB and Bayer CFA)
	 Single-tap (1X-1Y) progressive-scan
	Line-scan cameras and contact imaging sensors:
	 Grayscale and color RGB

Camera pixel formats supported	Mono8, Mono10, Mono12, Mono14, Mono16 ReverYV12, ReverYV14, ReverYV16, where YV = CR, RC, CR, et RC,
	• BayerXX8, BayerXX10, BayerXX12, BayerXX14, BayerXX16 where XX = GR, RG, GB, or BG
	RGB8, RGB10, RGB12, RGB14, RGB16
	• RGBA8, RGBA10, RGBA12, RGBA14, RGBA16
	YCbCr601_422_8, YCbCr601_422_10 YCbCr700_422_10
	• YCbCr709_422_8, YCbCr709_422_10
	• YUV422_8, YUV422_10
	• Raw
Area-scan camera control	
Trigger	 Precise control of asynchronous reset cameras, with exposure control.
	 Support of camera exposure/readout overlap.
	• Support of external hardware trigger, with optional delay and trigger decimation.
Strobe	 Accurate control of the strobe position for strobed light sources.
	Support of early and late strobe pulses.
Line-scan camera control	
Scan/page trigger	 Precise control of start-of-scan and end-of-scan triggers.
	 Support of external hardware trigger, with optional delay.
	• Support of infinite acquisition, without missing line, for web inspection applications.
Line trigger	 Support for quadrature motion encoders, with programmable noise filters, selection of acquisition direction and backward motion compensation.
	• Rate Converter tool for fine control of the pixel aspect ratio: Rate Conversion Ratio in the
	range 0.001 to 1000 with an accuracy better than 0.1%.
	Rate Divider tool
Line strobe	Accurate control of the strobe position for strobed light sources.
On-board processing	
On-board memory	1 GB
Image data stream processing	 Unpacking of 10-/12-/14-bit to 16-bit with selectable justification to LSb or MSb
	 Optional swap of R and B components
	Little endian conversion
Flat-field correction	Only available with the '1-camera' and '1-camera, line-scan' firmware variants
Input LUT (Lookup Table)	Available on all the firmware variants but '1-camera, 4-data-stream':
	 Monochrome 8-bit to 8-bit transformation
	 Monochrome 10-bit to 8-, 10- or 16-bit transformations
	 Monochrome 12-bit to 8-, 12- or 16-bit transformations
Bayer CFA to RGB decoder	 '1-camera' firmware variant:
	 3x3 linear interpolation method
	 3x3 median-based interpolation method
	 '2-camera' firmware variant:
	 5x5 gradient-based interpolation method (for one camera only)
Data stream statistics	Measurement of:
	 Frame rate (Area-scan only)
	– Line rate
	- Data rate
	Configurable averaging interval

Event signaling and counting	 The application software can be notified of the occurrence of various events:
	 Standard event: the EVENT_NEW_BUFFER event notifies the application of newly filled buffers
	 A large set of custom events
	Custom events sources:
	 I/O Toolbox events
	 Camera and Illumination control events
	 CoaXPress data stream events
	 CoaXPress host interface events
	 Each custom event is associated with a 32-bit counter that counts the number of occurrences
	• The last three 32-bit context data words of the event context data can be configured with event-specific context data:
	 Event-specific data
	 State of all System I/O lines sampled at the event occurrence time
	 Value of any event counter
General Purpose Inputs and Outputs	
Number of lines	20 I/O lines:
	• 4 differential inputs (DIN)

4 singled-ended TTL inputs/outputs (TTLIO)

A/B signals of a motion encoder

• Configurable filter time constants:

- for IIN lines: 500 ns, 1 μs, 2 μs, 5 μs, 10 μs

Non-isolated, +12V, 1A, with electronic fuse protection

• Any I/O input lines can be used by any LIN tool of the I/O Toolbox

pole LVTTL, TTL, 5V CMOS drivers or LVTTL, TTL, 3V CMOS receivers

• IOUT: Isolated contact outputs compatible with 30V / 100mA loads

differential line drivers and complementary TTL drivers

free contacts, solid-state relays and opto-couplers

• Glitch removal filter available on all System I/O input lines

- for DIN and TTLIO lines: 50 ns, 100 ns, 200 ns, 500 ns, 1 μs

• Selected pairs of I/O input lines can be used by any QDC tool of the I/O toolbox to decode

• DIN: High-speed differential inputs, up to 5 MHz, compatible with ANSI/EIA/TIA-422/485

• TTLIO: High-speed 5V-compliant TTL inputs or LVTTL outputs, compatible with totem-

• IIN: Isolated current-sense inputs with wide voltage input range up to 30V, compatible with totem-pole LVTTL, TTL, 5V CMOS drivers, RS-422 differential line drivers, potential

NOTE: IIN and IOUT lines provide a functional isolation grade for the circuit technical protection. It does not provide an isolation that can protect a human being from electrical

8 isolated inputs (IIN) 4 isolated outputs (IOUT)

shock!

Yes

Usage

Electrical specifications

Filter control

Polarity control

Power output

The I/O Toolbox is a configurable interconnection of tools that generates events (usually triggers):
 Line Input tool (LIN): edge detector delivering events on rising or falling edges of any selected input line.
 Quadrature Decoder tool (QDC): a composite tool including:
 A quadrature edge detector delivering events on selected transitions of selected pairs of input lines.
 An optional backward motion compensator for clean line-scan image acquisition when the motion is unstable.
 A 32-bit up/down counter for delivering a position value.
 Device Link Trigger tool (DLT): delivers an event on reception of a valid high-speed CoaXPress 2.0 connection trigger packet message from the remote device.
 User Actions Scheduler tool (UAS): to delegate the execution of 'User Actions' at a scheduled time or encoder position. Possible user actions include setting low/high/toggle any bit of the User Output Register or generation of any User Events.
• Delay tool (DEL): to delay up to 16 events from one or two I/O toolbox event sources, by a programmable time or number of motion encoder ticks (any QDC events).
• Divider tool (DIV): to generate an event every nth input events from any I/O toolbox event source.
 Multiplier/divider tool (MDV): to generate m events every d input events from any I/O toolbox event source.
 The 'Input Tools' (LIN, QDC, DLT and UAS) can be further processed by the 'Event Tools' (DEL, DIV and MDV) to generate any of the following "trigger" events:
– The "cycle trigger" of the Camera and Illumination controller
 The "cycle sequence trigger" of the Camera and Illumination controller
 The "start-of-scan trigger" of the Acquisition Controller (line-scan only)
 The "end-of-scan trigger" of the Acquisition Controller (line-scan only)
Determined by the selected firmware variant:
• '1-camera': 8 LIN, 1 QDC, 1 UAS, 2 DEL, 1 DIV, 1 MDV, 2 C2C
• '2-camera': 8 LIN, 2 QDC, 1 UAS, 2 DEL, 2 DIV, 2 MDV, 2 C2C
• '3-camera': 8 LIN, 2 QDC, 1 UAS, 2 DEL, 2 DIV, 2 MDV, 2 C2C
 '4-camera': 8 LIN, 4 QDC, 1 UAS, 4 DEL, 4 DIV, 4 MDV, 2 C2C
• '1-camera, 4-data-stream': 8 LIN, 1 QDC, 1 UAS, 2 DEL, 1 DIV, 1 MDV, 2 C2C
• '1-camera, line-scan': 8 LIN, 1 QDC, 1 UAS, 2 DEL, 1 DIV, 1 MDV, 3 C2C
• '2-camera, line-scan': 8 LIN, 2 QDC, 1 UAS, 2 DEL, 2 DIV, 2 MDV, 3 C2C
• '4-camera, line-scan': 8 LIN, 4 QDC, 1 UAS, 4 DEL, 4 DIV, 4 MDV, 3 C2C
• '1-slm-camera': 8 LIN, 1 QDC, 1 UAS, 2 DEL, 1 DIV, 1 MDV, 3 C2C
 '1-sls-camera': 8 LIN, 1 QDC, 1 UAS, 2 DEL, 1 DIV, 1 MDV, 3 C2C
• 1-SIS-Califera . 8 LIN, 1 QDC, 1 0AS, 2 DEL, 1 DIV, 1 MDV, 3 CZC
 Accurate synchronization of the trigger and the start-of-exposure of multiple grabber- controlled area-scan cameras.
• Accurate synchronization of the start-of-cycle, start-of-scan and end-of-scan of multiple

 the same card the same card to different cards in the same PC (requires on a ccessory cable such as the "3303 C2C-Link Ribbo Cable" or custom-made C2C-Link (able) to different cards in different PC (Sequires one "1636 interPC C2C-Link kdapter" for each PC and one PJ 45 CAT 5 STP straight LAN cable for each adapter but the last one) Maximum distance: 60 cm inside a PC 2.5 MHz for configurations using a single PC, or up to 10 PCs and 100 m total C2C-Link cable length Maximum trigger rate: 	Specification	C2C-Link synchronizes cameras connected to:
 to different cards in the same PC (requires an accessory cable such as the "3303 C2C-Link Ribbon Cable" or a custom-made C2C-Link cable to different Cards in different C3C (sequires on "1636 interPC C2C-Link Adapter" for each PC and one RJ 45 CAT 5 STP straight LAN cable for each adapter but the last one? Maximum distance: 60 cm inside a PC 1200 m cumulated adapter to adapter cable length Maximum trigger rate: 2.5 MHz for configurations using a single PC, or up to 10 PCs and 100 m total C2C-Link cable length Trigger propagation delay from master to slave devices: Less than 10 ns for cameras on the same card or on different PCs (S PCs and 400 m total C2C-Link cable length) Software Host PC Operating System Microsoft Windows 11, 10, 81, 7 for x86-64 (64-bit) processor architectures Linux for x86-64 (64-bit) and AArch64 (64-bit) processor architectures macO5 for x86-64 (64-bit) and AArch64 (64-bit) processor architectures Microsoft Windows 11, 10, 81, 7 for x86-64 (64-bit) processor architectures Linux for x86-64 (64-bit) and AArch64 (64-bit) processor architectures MacG for x86-64 (64-bit) and AArch64 (64-bit) processor architectures macO5 for x86-64 (64-bit) and AArch64 (64-bit) applications "a 'x86_64 'dynamic library designed to be used with LSO-compliant C/C++ compilers for the development of x86-64 (64-bit) applications "a 'x86_64 'dynamic library designed to be used with LSO-compliant C/C++ compilers for the development of AArch64 (64-bit) applications "a 'x86_64 'dynamic library designed to be used with LSO-compliant C/C++ compilers for the development of AArch64 (64-bit) applications "a 'x86_64 'dynamic library designed to be used with LSO-c	Specification	-
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- 2.5 MHz for configurations using a single PC, or up to 10 PCs and 100 m total C2C-Link cable length - 200 kHz for configurations up to 32 PCs and 1200m total C2C-Link cable length - Trigger propagation delay from master to slave devices: - Less than 10 ns for cameras on the same card or on different Cards in the same PC - Less than 265 ns for cameras on different cards in different PCs (3 PCs and 40m total C2C-Link cable length) Software Host PC Operating System • Microsoft Windows 11, 10, 8.1, 7 for x86-64 (64-bit) processor architecture Linux for x86-64 (64-bit) and AArch64 (64-bit) processor architectures • macOS for x86-64 (64-bit) and AArch64 (64-bit) processor architectures APIs • EGrabber class, with (++ and .NET APIs: .NET assembly designed to be used with development environments compatible with .NET frameworks version 4.0 or higher GeniCam GenTL producer libraries compatible with SO-compliant C/C++ compilers for the development of x86-64 (64-bit) applications Environmental conditions - 'sarch64' dynamic library designed to be used with ISO-compliant C/C++ compilers for the development of x86-64 (64-bit) applications Storage ambient air temperature 0 °C to +55 °C / +32 °F to +131 °F Operating ambient air temperature 0 °C to +70 °C / 4 °F to +158 °F Storage ambient air temperature 0 °C to +70 °C / 4 °F to +158 °F Storage ambient air temperature 0 °C to +70 °C / 4 °F to +158 °F		
- 200 kHz for configurations up to 32 PCs and 1200m total C2C-Link cable length - Trigger propagation delay from master to slave devices: - Less than 10 ns for cameras on the same card or on different cards in the same PC - Less than 26 ns for cameras on different cards in different PCs (3 PCs and 40m total C2C-Link cable length) Software Host PC Operating System • Microsoft Windows 11, 10, 8.1, 7 for x86-64 (64-bit) processor architectures APIs • Microsoft Windows 11, 10, 8.1, 7 for x86-64 (64-bit) processor architectures APIs • EGrabber class, with C++ and .NET APIs: .NET assembly designed to be used with development environments compatible with .NET frameworks version 4.0 or higher • GeniCam GenTL producer libraries compatible with NET frameworks version 4.0 or higher • S66_64 (4)-bit) applications Environmental conditions • 'aarch64 'qnamic library designed to be used with ISO-compliant C/C++ compilers for the development of x86-64 (64-bit) applications Doperating ambient air temperature 0 °C to +55 °C / +32 °F to +131 °F Operating ambient air temperature 0 °C to 9 70 °C / 4 °F to +158 °F Storage ambient air temperature 0 °C to 9 70 °C / 4 °F to +158 °F Storage ambient air humidity 10% to 90% RH non-condensing Certifications • European Council EMC Directive 2014/30/EU Eldcr emission • EN 55032/2015 / CISPR 32:2012		– 2.5 MHz for configurations using a single PC, or up to 10 PCs and 100 m total C2C-Link
 Trigger propagation delay from master to slave devices: Less than 10 ns for cameras on the same card or on different cards in the same PC Less than 265 ns for cameras on different cards in different PCs (3 PCs and 40m total C2C-Link cable length)		-
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RoHSEuropean Union Directive 2015/863 (ROHS3)	Flammability	PCB compliant with UL 94 V-0
	RoHS	European Union Directive 2015/863 (ROHS3)

REACH	European Union Regulation 1907/2006
WEEE	Must be disposed of separately from normal household waste and must be recycled according to local regulations
Ordering Information	
Product code - Description	• 1633 - Coaxlink Quad G3
Optional accessories	• 1625 - DB25F I/O Adapter Cable
	 1636 - InterPC C2C-Link Adapter
	• 3303 - C2C-Link Ribbon Cable
	• 3304 - HD26F I/O Adapter Cable



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